

L Number	Hits	Search Text	DB	Time stamp
-	1184	(711/103).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 07:27
-	1351	(711/170).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:22
-	0	("(memorynear5(interface\$1orterminal\$1orport\$1))with(dieorchip)").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:34
-	6869	(memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:58
-	1184	(711/103).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:36
-	1351	(711/170).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:36
-	2675	(714/718).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:37
-	418	(326/37).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38
-	1148	(326/38).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38
-	1154	(326/39).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38
-	1260	(326/41).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38
-	344	(326/47).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38
-	3389	(365/230.06).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38
-	2958	(365/201).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38
-	3448	(365/200).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:38

-	756	(365/189.02).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:39
-	233	(365/189.03).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:39
-	658	(365/230.02).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:39
-	1276	(365/51).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:39
-	1779	(365/63).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:39
-	214	(345/519).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	261	(345/520).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	53	(345/533).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	76	(345/554).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	75	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((711/103).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	59	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((711/170).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	91	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((714/718).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	1	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((326/37).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	18	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((326/38).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40
-	12	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((326/39).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:40

-	15	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((326/41).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	2	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((326/47).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	106	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/230.06).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	159	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/201).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	117	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/200).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	35	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/189.02).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	25	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/189.03).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	38	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/230.02).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	75	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/51).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	105	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((365/63).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:41
-	38	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((345/519).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:42
-	19	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((345/520).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:49
-	2	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((345/533).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:56
-	9	((memory near5 (terminal\$1 or interface\$1 or port\$1)) with (die or chip)) and ((345/554).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:57
-	275	(mode near5 interface\$1) with (die or chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:59

-	0	((345/520).CCLS.) and ((mode near5 interface\$1) with (die or chip))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:59
-	0	((345/533).CCLS.) and ((mode near5 interface\$1) with (die or chip))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:59
-	0	((345/554).CCLS.) and ((mode near5 interface\$1) with (die or chip))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 08:59
-	1	((345/519).CCLS.) and ((mode near5 interface\$1) with (die or chip))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:00
-	39711	test\$3 with (die or chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:01
-	1475	test\$3 with (die or chip) with interface\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:03
-	118	test\$3 with (die or chip) with interface\$1 with mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:04
-	17717223	@ad<=19990416	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:28
-	53	(test\$3 with (die or chip) with interface\$1 with mode) and @ad<=19990416	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:25
-	70232	different near10 (mode or interfaces)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:26
-	585	different near10 (mode or interfaces) with (die or chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:26
-	2	((different near10 interfaces) with (die or chip)) with mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:27
-	52	(interfaces) with (die or chip) with mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 09:28
-	21	@ad<=19990416 and ((interfaces) with (die or chip) with mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:49
-	5117	interfaces\$ same memory same mode\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:11

-	13	((711/103).CCLS.) and (interfaces\$ same memory same mode\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:09
-	2	@ad<=19990416 and (((711/103).CCLS.) and (interfaces\$ same memory same mode\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:12
-	704	interfaces\$ with memory with mode\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:12
-	22	interfaces\$ with memory with mode\$1 with flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:15
-	5	@ad<=19990416 and (interfaces\$ with memory with mode\$1 with flash)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:18
-	141	(interfaces\$ or terminals\$ or ports\$) with memory with mode\$1 with flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:15
-	60	@ad<=19990416 and ((interfaces\$ or terminals\$ or ports\$) with memory with mode\$1 with flash)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:30
-	50746	(plurality or multiple) near5 interface\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 11:12
-	1025	((plurality or multiple) near5 interface\$1) with mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:47
-	6	((plurality or multiple) near5 interface\$1) with mode with flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:36
-	18	((plurality or multiple) near5 interface\$1) with mode with (die or chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:41
-	8	flash with interfaces with modes	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:43
-	0	((345/519).CCLS.) and (flash with interfaces with modes)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:47
-	3	((345/519).CCLS.) and (((plurality or multiple) near5 interface\$1) with mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:47
-	141	@ad<=19990416 and ((345/519).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:49

-	4	((plurality or multiple) near5 interface\$1) with (("same" or "common") near2 die)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 11:19
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